

10/560756

IAP2004

15 DEC 2005

DOCKET NO.: 96790P515

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

YASUHIRO ODA, ET AL.

Application No.:

Filed:

For: **HETEROSTRUCTURE BIPOLAR
TRANSISTOR**

Art Group:

Examiner:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR PRIORITY

Sir:

Applicant respectfully requests a convention priority for the above-captioned application, namely:

| <u>COUNTRY</u> | <u>APPLICATION NUMBER</u> | <u>DATE OF FILING</u> |
|----------------|-------------------------------|-----------------------|
| JP | 2004-195533 | 1 July 2004 |
| JP | 2004-265753 | 13 September 2004 |

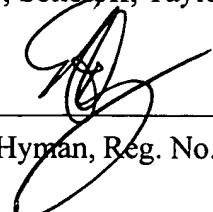
☐ A certified copy of the document is being submitted herewith.

Respectfully submitted,

Blakely, Sokoloff, Taylor & Zafman LLP

Dated: 12/13/05

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